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APR 21 2005

In re Patent Application of:  
SANCHES ET AL.

Serial No. 09/915,761

Confirmation No. 7474

Filing Date: JULY 26, 2001

For: SIGNAL PROCESSOR EXECUTING  
VARIABLE SIZE INSTRUCTIONS  
USING PARALLEL MEMORY BANKS,  
AND CORRESPONDING METHOD

Examiner: A. LI

Art Unit: 2183

AMENDMENT AFTER FINALMS AF  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Examiner's final Office Action of December 21, 2004 and the Advisory Action of March 16, 2005, please enter the amendments and remarks set forth below. No new issues are raised by this amendment.

ENTER

AL

4-28-2005